AMENDMENTS TO THE SPECIFICATION

Please amend the specification as indicated below:

[0011] According to the present invention, a redundant system having two switch routes~~:~~ ~~includes~~ comprises: N ~~(N >= 1)~~ (where, N ≥ 1) input selectors[[.]], (each of which selects one of the two switch routes to connect N input lines to the selected ~~one~~ switching route depending on a system switching signal); two switch sections provided for respective ones of the two switch routes[[,]] (each of the switch sections having N input ports and N output ports and comprising N buffers, where each ~~of which~~ switch section comprises M ~~(M>=2)~~ (where, M ≥ 2) priority queues for storing input packets classified under M priorities); M priority output queues corresponding to respective ones of the M priorities; an output selector for selecting one of two priority queues, corresponding to the two switch sections, for each of the M priorities, ~~corresponding to respective ones of the two switch sections~~ to store an output of the selected one, priority queue, into a corresponding one of the M priority output queues; and a controller for instructing the output selector to select one of the two priority queues, for each of the M priorities, corresponding to respective ones of the two switch sections, where instructing depends ~~depending~~ on the system switching signal and a packet storing status of each of the M priority queues.

[0012] When the one of the two switch routes is switched to the other <u>switch route, as</u>

<u>indicated</u> by the system switching signal, the controller monitors a packet storing status of each

of the M priority queues; and if the one of the two priority queues (corresponding to respective

ones of the two switch sections) becomes empty, then the controller instructs the output selector

to select the other of the two priority queues to store an output of the selected one into a corre-

sponding one of the M priority output queues.

[0015] According to an aspect of the present invention, a packet switching system

having two switch routes, ~~includes~~ <u>comprises</u>: N ~~(N >= 1)~~ <u>(where, N ≥ 1)</u> input selectors, each of

which selects one of the two switch routes to connect N input lines to the selected ~~one~~ <u>switch</u>

<u>route</u> depending on a system switching signal; two switch sections provided for respective ones

of the two switch routes, each of the switch sections having N input ports and N output ports and

comprising N buffers, each of which comprises[[:]] a high-priority queue for storing input

packets having a high priority; and a low-priority queue for storing input packets having a low

priority; a high-priority output selector for selecting one of two high-priority queues corre-

sponding to respective ones of the two switch sections; a low-priority output selector for

selecting one of two low-priority queues corresponding to respective ones of the two switch

sections: a high-priority output queue for storing an output of the selected one of the two high-

priority queues; a low-priority output queue for storing an output of the selected one of the two

low-priority queues: and a controller controlling the high-priority and low-priority output

selectors depending on the system switching signal and a packet storing status of each of the

high-priority and low-priority queues.